

APR 25 2005

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24309 7590 02/28/2005

XILINX, INC
ATTN: LEGAL DEPARTMENT
2100 LOGIC DR
SAN JOSE, CA 95124

04/26/2005 WABDEL3 00000113 240040 10082518

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Pat Slaback (Depositor's name)
Pat Slaback (Signature)
April 25, 2005 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/082,518	02/22/2002	L. James Hwang	X-1002 US	5974

TITLE OF INVENTION: METHOD AND SYSTEM FOR INTEGRATING CORES IN FPGA-BASED SYSTEM-ON-CHIP (SOC)

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	05/31/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
WHITMORE, STACY	2825	716-01600

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 Pablo Meles

2 LeRoy D. Maunu

3

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

XILINX, INC.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

San Jose, California 95124

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

4a. The following fee(s) are enclosed:

- ☐ Issue Fee
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5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☒ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

The Director of the USPTO is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above.
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Authorized Signature

Typed or printed name

Kim Kanzaki

Date April 25, 2005

Registration No. 37,652

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P. O. Box 1450

Alexandria, Virginia

22313-1450



Inventor(s): L. James Hwang et al

Assignee: **XILINX, INC.**

Serial No.: 10/082,518

conf.no. 5974

Filed: February 22, 2002

Title: Method and System for Integrating CORES in FPGA-Based System-On-Chip (SoC)

Docket No.: X-1002 US

Enclosed: Return Receipt Postcard

Fee Transmittal - Part B

Date: April 25, 2005

VIA FACSIMILE 703-746-4000

Atty/Sec: KK/pbs